UTILITY	_
PATENT APPLICATION	N
TRANSMITTAL	

Attorney Docket No.

217713US2

First Inventor or Application Identifier

Jun TATSUTA

SEMICONDUCTOR-CHIP MOUNTING-SUBSTRATE AND METHOD OF MANUFACTURING THE SAME

Assignee Name: Matsushita Electric Works, Ltd.

Assignee Address: 1048 Oaza-Kadoma, Kadoma-shi, Osaka 571-8686, Japan

Δ APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent application contents	Assistant Commissioner for Patents ADDRESS TO: Box Patent Application Washington, DC 20231			
1 ■ Fee Transmittal Form (e.g. PTO/SB/17)	ACCOMPANYING APPLICATION PARTS			
(Submit an original and a duplicate for fee processing) 2. Specification Total Sheets 33	7. ☐ Assignment Papers (cover sheet & document(s)) 8. ☐ Application Data Sheet. See 37 CFR 1.76			
3. ■ Formal Drawing(s) Total Sheets 6	9. □ 37 C.F.R. §3.73(b) Statement □ Power of Attorney 10. □ English Translation Document (if applicable)			
4. ■ Oath or Declaration Total Pages 5 a. ■ Newly executed (original) - Copy from a prior application (37.0 F.B. \$1.63(d))	 Information Disclosure Statement (IDS)/PTO-1449			
b. Copy from a prior application (37 C.F.R, §1.63(d)) (for continuation / divisional w/ box 17 completed) i. DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b).	14. Certified Copy of Priority Document(s) (if foreign priority is claimed) 15. Applicant claims small entity status. See 37 CFR 1.27			
 5. □ CD-ROM or CD-R in duplicate, large table or Computer Program (<i>Appendix</i>) 6. □ Nucleotide and/or Amino Acid Sequence Submission (<i>if applicable, all necessary</i>) a. □ Computer Readable Form (CRF) 	16. ■ Other: Statement of Relevancy			
b. Specification or Sequence Listing on: i. □ CD-ROM or CD-R (2 copies); or ii. □ Paper c. □ Statements verifying identity of above copies				
7. If a CONTINUING APPLICATION, check appropriate box, and supp Continuation Divisional Continuation-i Prior application information: Examiner: CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior publicated a part of the accompanying continuation or divisional application and is then a portion has been inadvertently omitted from the submitted application parts.	in-part (CIP) of prior application no.: Group Art Unit:			
8. Amend the specification by inserting before the first line the ser ☐ This application is a ☐ Continuation ☐ Division of application Serial No. Filed on ☐ Which was published in English ☐ Which was not published in English	ntence: □ Continuation-in-part (CIP)			
□ This application claims priority of provisional application Serial No. Filed				
19. CORRESPONDENCE ADDRESS				

Name: Marvin J. Spivak 24,913 Registration No.: 12/27/01 Signature: Date: Name: Registration No.:

22850 (703) 413-3000 FACSIMILE: (703) 413-2220



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

'INVENTOR(S) Jun TATSUTA, et al.

SERIAL NO:

New Application

FILING DATE: Herewith

FOR:

SEMICONDUCTOR-CHIP MOUNTING SUBSTRATE AND METHOD OF MANUFACTURING THE

SAME

FEE TRANSMITTAL

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	26 - 20 =	6	× \$18 =	\$108.00
INDEPENDENT CLAIMS	2 - 3 =	0	× \$84 =	\$0.00
MULTIPLE DEPENDENT	NT CLAIMS (If app	olicable)	+ \$280 =	\$0.00
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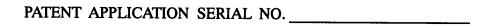
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Respectfully Submitted,

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Marvin J. Spivak

Registration No. C. Irvin McClelland Registration Number 21,124



U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE FEE RECORD SHEET

12/28/2001 MABDI1 00000023 10026639

01 FC:101 02 FC:103

740.00 DP 108.00 DP

PTO-1556 (5/87)